

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
18 December 2003 (18.12.2003)

PCT

(10) International Publication Number
WO 2003/104976 A3

(51) International Patent Classification⁷: **G06F 9/24**, 9/445

(21) International Application Number:
PCT/IB2003/002093

(22) International Filing Date: 4 June 2003 (04.06.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
02012577.9 6 June 2002 (06.06.2002) EP

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(81) Designated States (national): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,

CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD,
SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US,
UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO,
SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

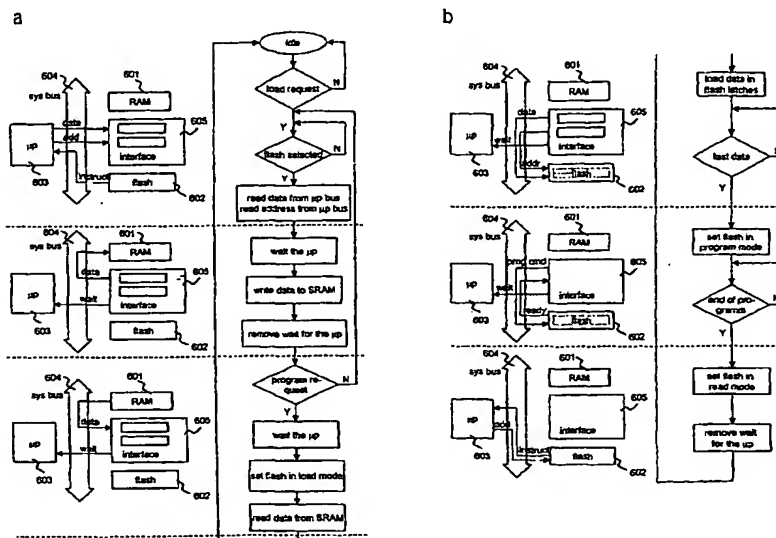
Published:

— with international search report
— before the expiration of the time limit for amending the
claims and to be republished in the event of receipt of
amendments

(88) Date of publication of the international search report:
3 June 2004

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: **METHOD AND APPARATUS FOR WRITING DATA TO A NON-VOLATILE MEMORY**



(57) **Abstract:** The invention is a method and a circuit for writing data (DATA) from a processor (603) to a non-volatile memory (602) embedded in an integrated circuit. The main objective is to optimize the use of this embedded non-volatile memory. The method comprises a number of steps: The data to be written to the non-volatile memory (602) is first transferred to a volatile memory (601). Thereafter, a wait signal (wait) will be send to the processor (603). Then, the data (DATA) will be transferred from the volatile memory (601) to the non-volatile memory (602). At last, the wait signal (wait) will be removed. Thus the non-volatile memory (602) can be used both as instruction memory and as RAM, which achieves the main goal of this invention. The corresponding circuitry is a complex integrated circuit equipped to execute the above functions.

INTERNATIONAL SEARCH REPORT

Application No
PCT/IB 03/02093A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F9/24 G06F9/445

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)
EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 974 528 A (KUO FANG-MING ET AL) 26 October 1999 (1999-10-26) the whole document	1-10, 12
X	DE 100 52 877 A (SAMSUNG ELECTRONICS CO LTD) 2 August 2001 (2001-08-02) the whole document	1-12
X	US 4 382 279 A (UGON MICHEL) 3 May 1983 (1983-05-03) the whole document	1-10
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Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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Date of the actual completion of the international search

9 March 2004

Date of mailing of the international search report

22/03/2004

Name and mailing address of the ISA

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INTERNATIONAL SEARCH REPORT

Application No
PCT/IB 03/02093

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>BROWN C ET AL: "SIMULTANEOUS CODE EXECUTION AND DATA STORAGE IN A SINGLE FLASH MEMORY CHIP FOR REAL TIME WIRELESS COMMUNICATION SYSTEMS" PROCEEDINGS OF THE 40TH MIDWEST SYMPOSIUM ON CIRCUITS AND SYSTEMS. MWSCAS '97. SACRAMENTO, CA, AUG. 3 - 6, 1997, MIDWEST SYMPOSIUM ON CIRCUITS AND SYSTEMS, NEW-YORK, NY: IEEE, US, vol. 2, 3 August 1997 (1997-08-03), pages 740-745, XP000789261 ISBN: 0-7803-3695-X page 742, right-hand column -page 743, left-hand column</p>	1,9
A	<p>DIPERT B ET AL: "DESIGNING AN UPDATABLE BIOS USING FLASH MEMORY" MICROPROCESSORS AND MICROSYSTEMS, IPC BUSINESS PRESS LTD. LONDON, GB, vol. 16, no. 8, 1992, pages 427-446, XP000330850 ISSN: 0141-9331</p>	

INTERNATIONAL SEARCH REPORT

Information on patent family members

Patent

Publication No

PCT/IB 03/02093

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